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	Application No.	Applicant(s)	
	10/602,816	MAKINO ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Demetrius R. Pretlow	2863	
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. This communication is responsive to <u>25 June 2003</u> .			
2. 🔯 The allowed claim(s) is/are <u>1-12</u> .			
3. 🗵 The drawings filed on 25 June 2003 are accepted by the Examiner.			
4.			
 Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No./Mail Date 0/2 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview Summary Paper No./Mail Dat 8), 7. ☐ Examiner's Amendr	e	

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DETAILED ACTION

Allowable Subject Matter

- 1. The best prior art of record particular Matsumoto et al. teach a sensor adjusting circuit for adjusting a digital sensor, whose circuit scale is small and which can maintain high accuracy in a wide adjustment range is provided. A sensor adjusting circuit for adjusting an analog input signal inputted from a sensor and outputting it as another analog output signal in accordance with a physical quantity to be sensed. However Matsumoto does not teach the claim limitations listed below.
- 2. Claims 1-12 are allowed.
- 3. The primary reason for the allowance of claims 1-4 is the inclusion of the limitations of an offset adjustment means for determining a measurement range of the amplified first voltage among a plurality of predetermined measurement ranges, and for outputting a second voltage corresponding to the determined measurement range; an addition means for subtracting the second voltage from the amplified first voltage, and for outputting the subtracted amplified first voltage, which is in the determined measurement range; and an output means for outputting the subtracted amplified first voltage to an outside circuit, and for informing the determined measurement range to the outside circuit, wherein the outside circuit calculates the physical quantity based on the subtracted amplified first voltage and the determined measurement range. It is these limitations found in each of the claims, as they are claimed in the combination, that has not been

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found, taught or suggested by the prior art of record which makes these claims allowable over the prior art.

- 4. The primary reason for the allowance of claims 6-12 is the inclusion of the method step of determining a measurement range of the first voltage among a plurality of predetermined measurement ranges, so that a second voltage corresponding to the determined measurement range is outputted; subtracting the second voltage from the first voltage; and outputting the subtracted first voltage and an information about the determined measurement range. It is these steps found in each of the claims, as it is claimed in the combination, that has not been found, taught or suggested by the prior art of record which makes these claims allowable over the prior art.
- 5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Demetrius R. Pretlow whose telephone number is (703) 272-2278. The examiner can normally be reached on 8-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Barlow can be reached on (703) 272-2269. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Demetrius R. Pretlow

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Patent Examiner

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